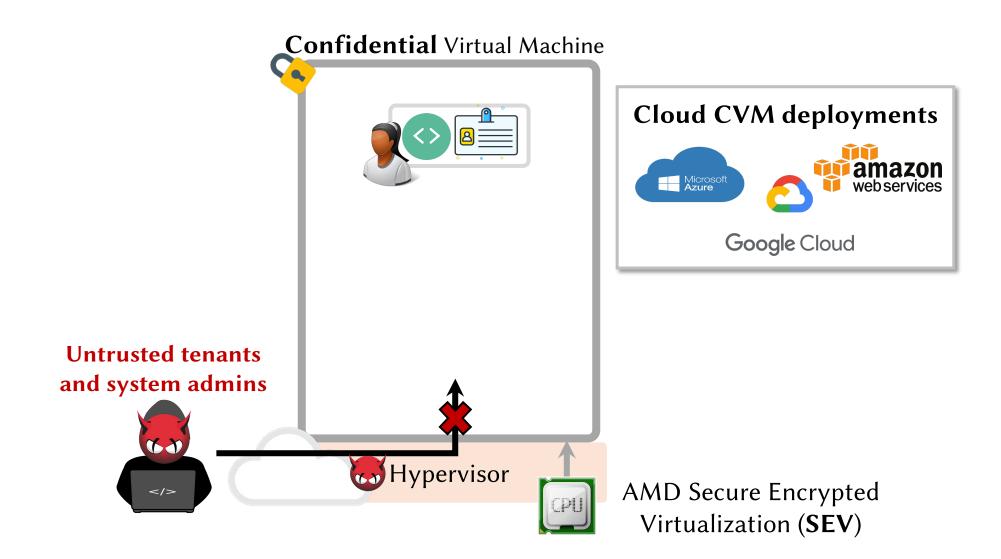
# VEIL: A Protected Services Framework for Confidential Virtual Machines

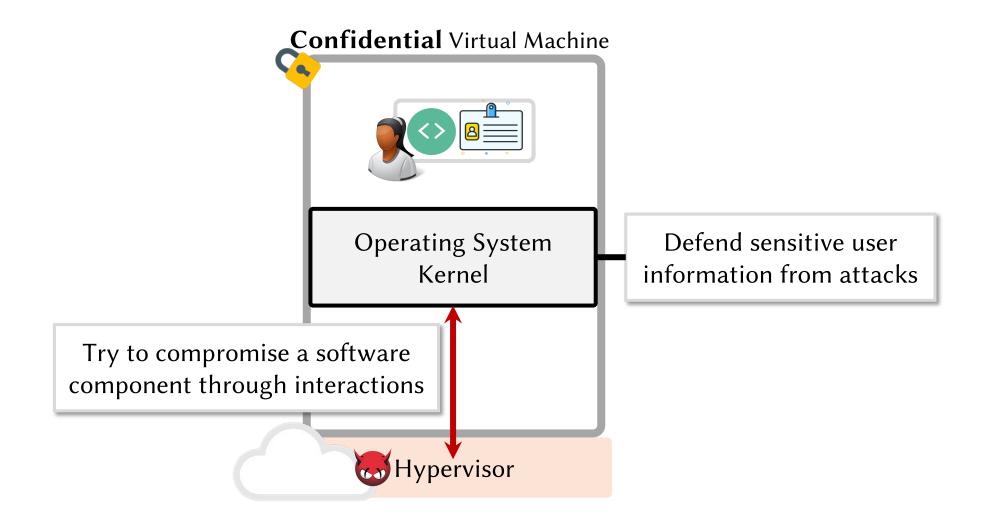
Adil Ahmad<sup>1</sup>, Botong Ou<sup>2</sup>, Congyu Liu<sup>2</sup>, Xiaokuan Zhang<sup>3</sup>, Pedro Fonseca<sup>2</sup>



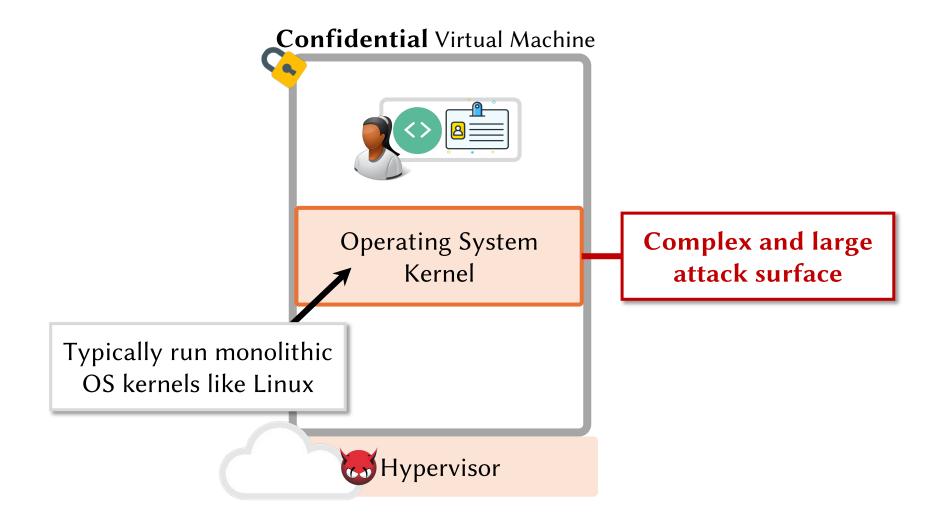
### **CVMs restrict data access from outside components**



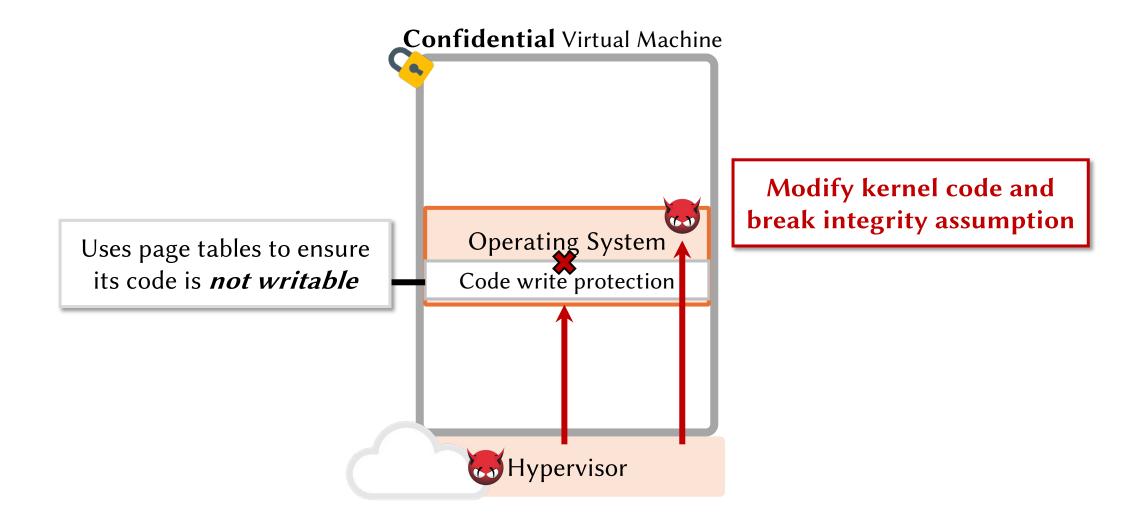
### CVMs rely on OS for defense against remaining attacks



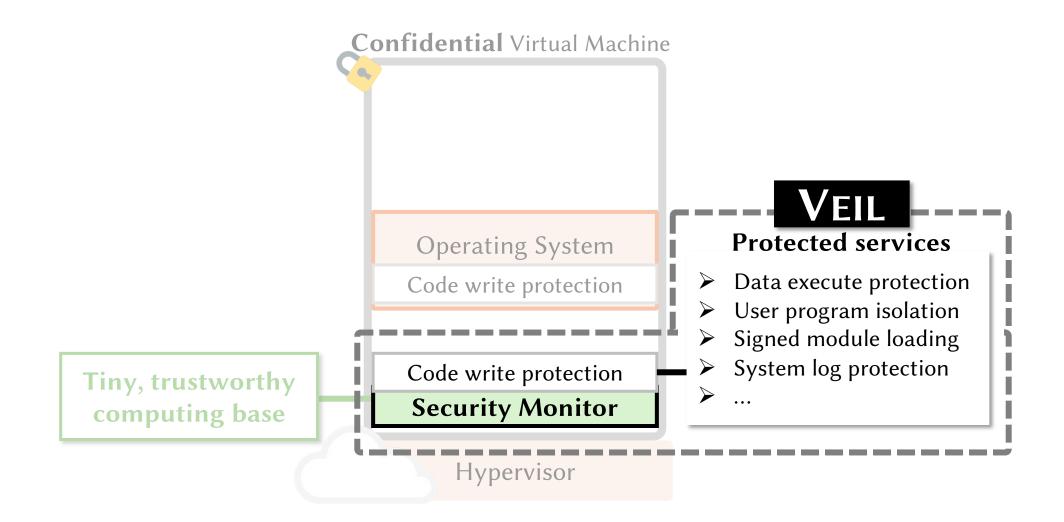
### Assumption of trust on the CVM OS is misplaced



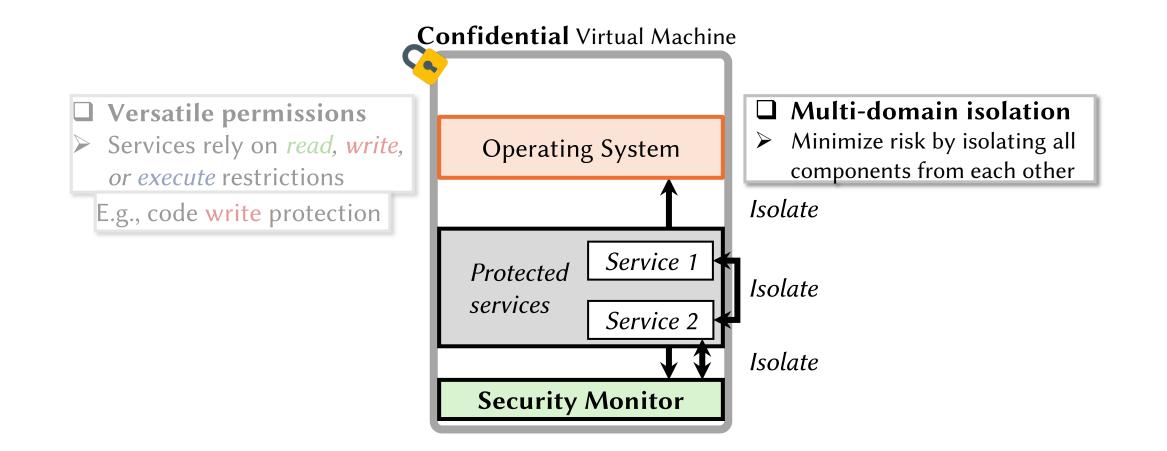
### Kernel code integrity as an *example* of misplaced trust



#### How can we solve the problem of misplaced trust?

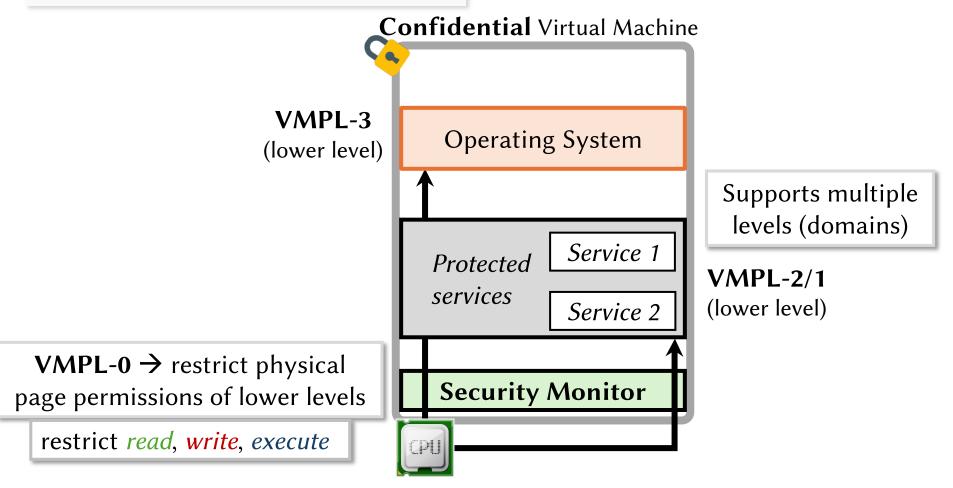


### Two requirements for VEIL's monitor and protected services

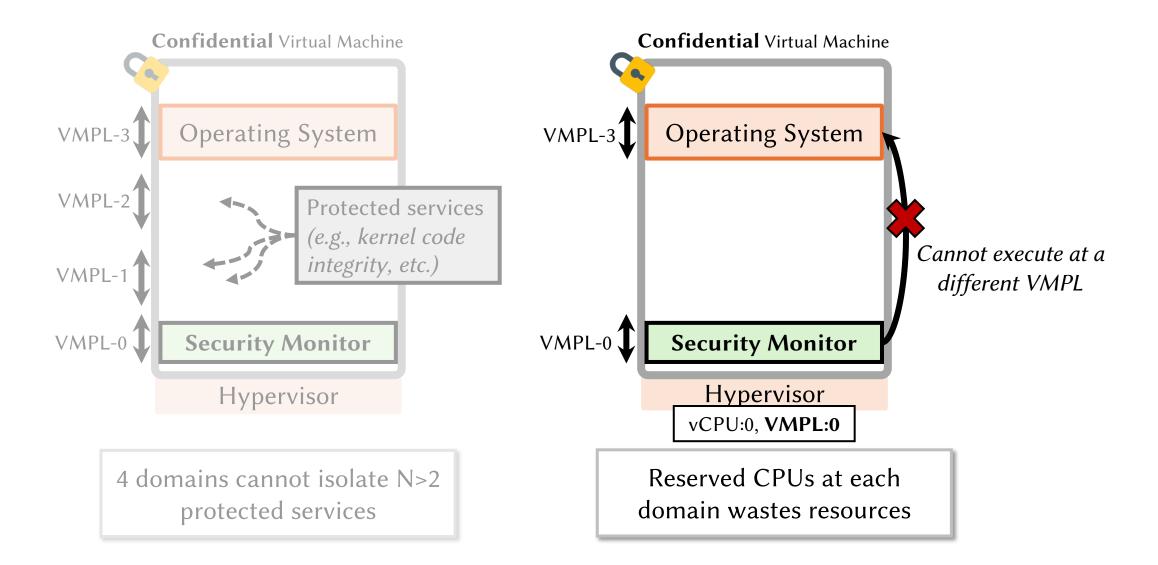


# **VEIL leverages Virtual Machine Privilege Levels (VMPL)**

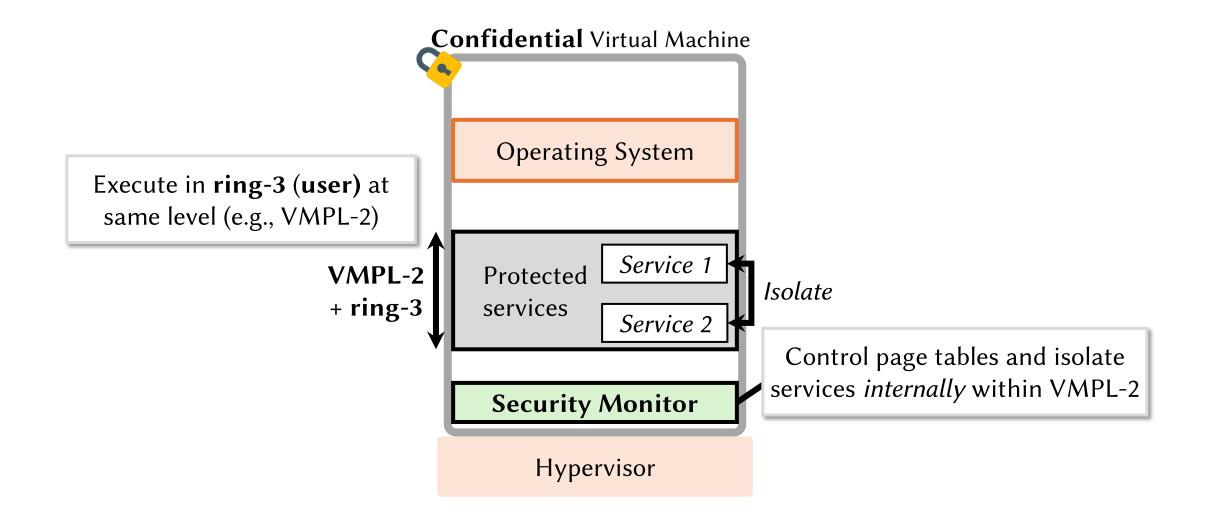
Available in AMD SEV-SNP servers



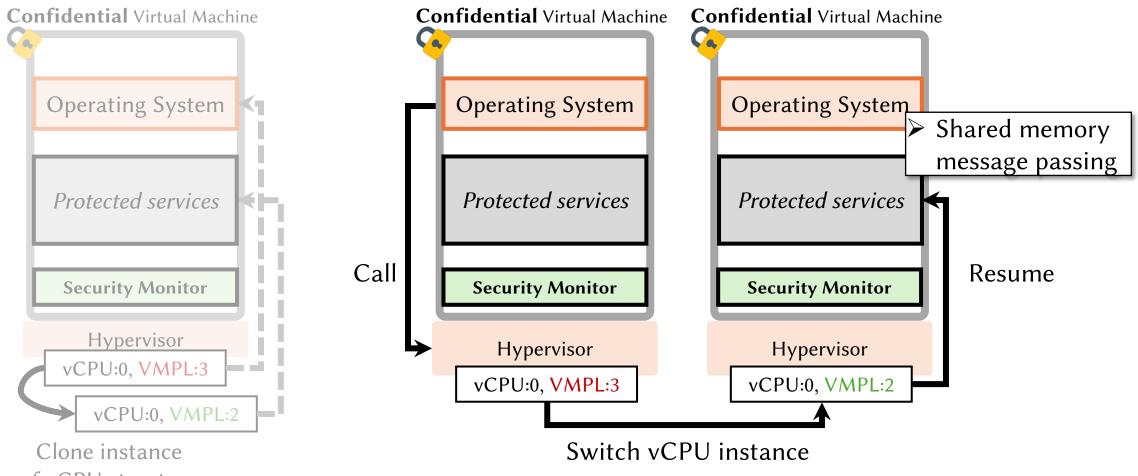
## What are the challenges in using VMPL for VEIL?



### 1: Combine VMPL-rings for numerous domains

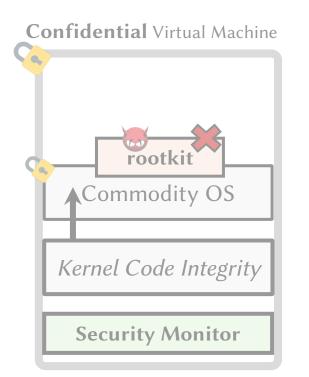


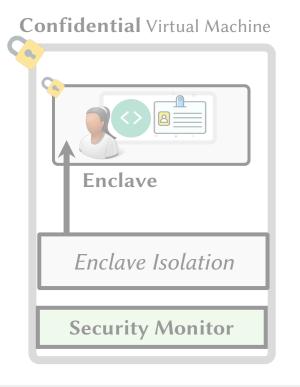
### 2: Replicate vCPU instances to avoid reservation

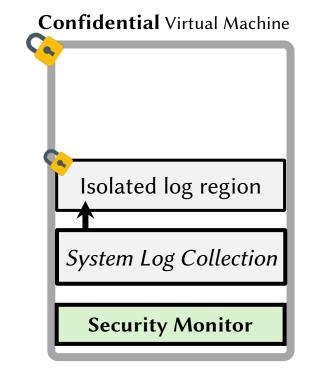


of vCPU structure

### **VEIL supports a diverse set of protected services**







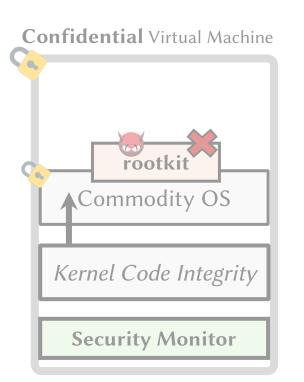
**Kernel code integrity** (prevent code injection/rewrite)

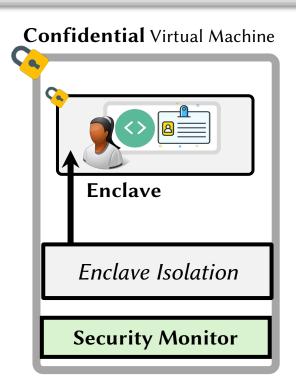
**Enclave isolation** (Isolate user programs from OS)

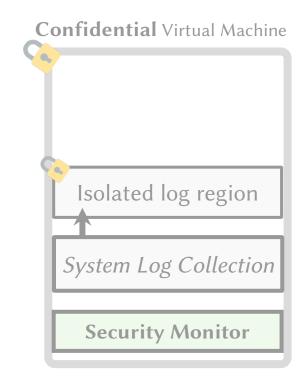
**Secure log collection** (store logs in protected regions)

### **VEIL** supports a diverse set of protected services

Will briefly present today







**Kernel code integrity** (prevent code injection/rewrite)

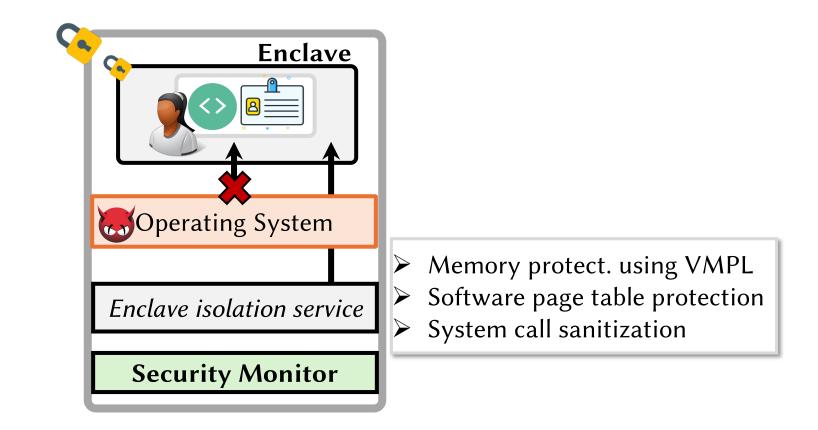
**Enclave isolation** (Isolate user programs from OS)

**Secure log collection:** (store logs in protected regions)

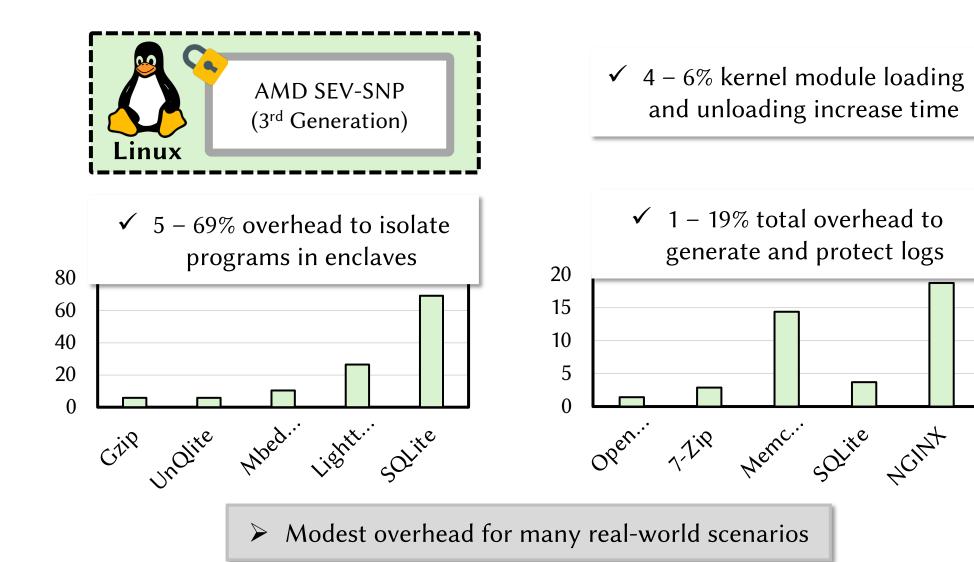
## Realize SGX-like enclaves using VEIL in CVMs



Intel Software Guard eXtensions (**SGX**)



#### What is VEIL's runtime performance overhead?



### Conclusion



